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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Chia-Lin Chen, et al.

Appl. No. 10/712,460

Filed: November 13, 2003

For: SEMICONDUCTOR WAFER
MANUFACTURING METHODS EMPLOYING
CLEANING DELAY PERIOD

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Group Art Unit: 2825

Examiner: Everhart, Caridad M.

Confirmation No.: 9316

TRANSMITTAL

Mail Stop Amendment
Commissioner For Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed are the following for filing in the above-identified patent application:

1. Response to Office Action dated October 21, 2004;
2. This transmittal page (in duplicate); and
3. A return postcard.

The Commissioner is hereby authorized to charge any fee deficiencies or credit any over payment to the Deposit Account of Haynes and Boone, LLP 08-1394.

Respectfully submitted,

Dave R. Holman
Registration No. 55,272

Date: 1/18/05
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R-87505.1

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

on 1-18-05

Bonnie Boyle

Signature of person mailing paper and fee



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EMPLOYING CLEANING DELAY PERIOD	§	

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Commissioner for Patents
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AMENDMENT IN RESPONSE TO OFFICE ACTION
MAILED OCTOBER 21, 2004

I. Introductory Comments

The present paper is being submitted in response to the Office Action mailed October 21, 2004.

No extension of time fees or any other fees are believed necessary for consideration of the present paper. However, if extension of time fees or any other fees are necessary, the extension of time is hereby requested, and the Commissioner is hereby authorized to charge extension of time fees and/or any other fees to Haynes and Boone, LLP's Deposit Account No. 08-1394.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 5 of this paper.